

Fig.1

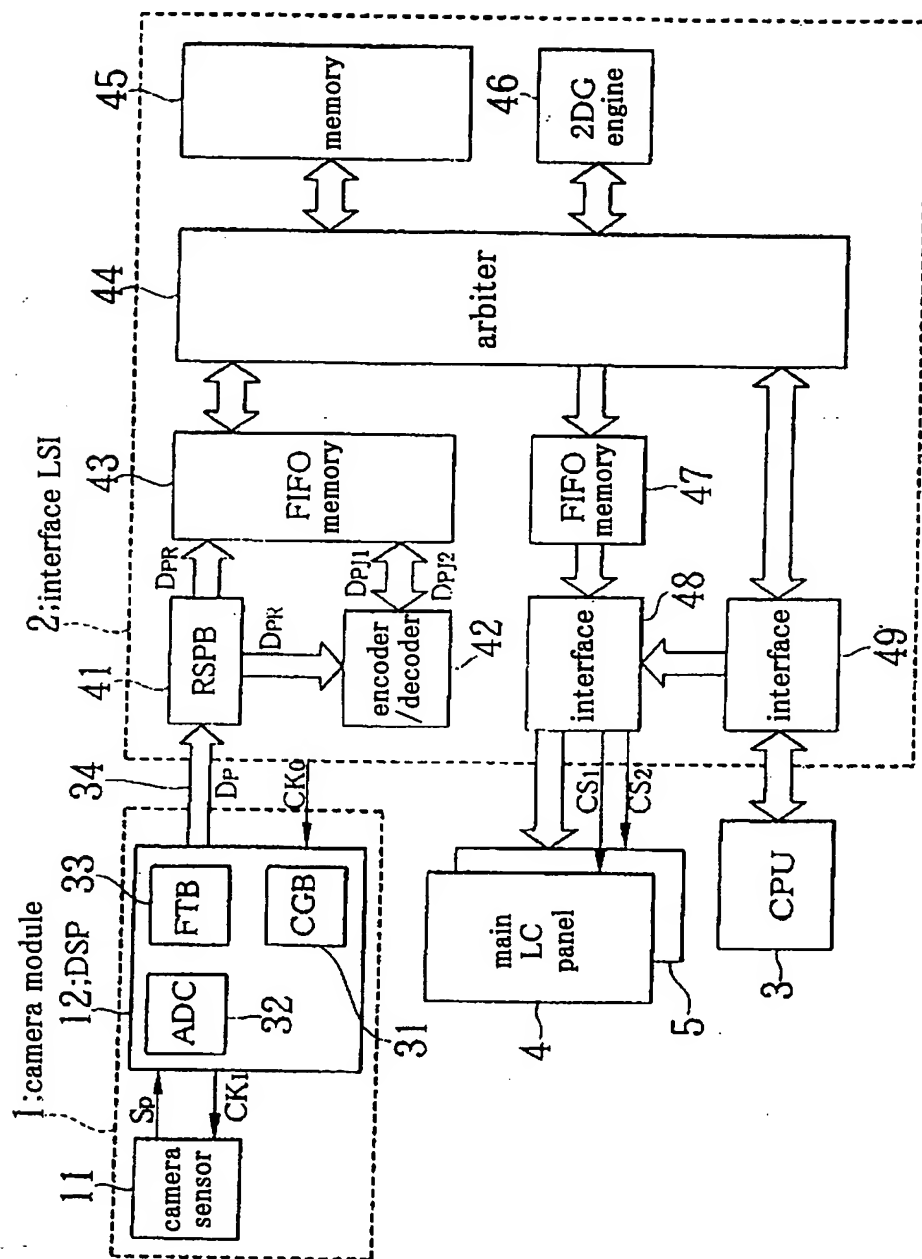


Fig.2

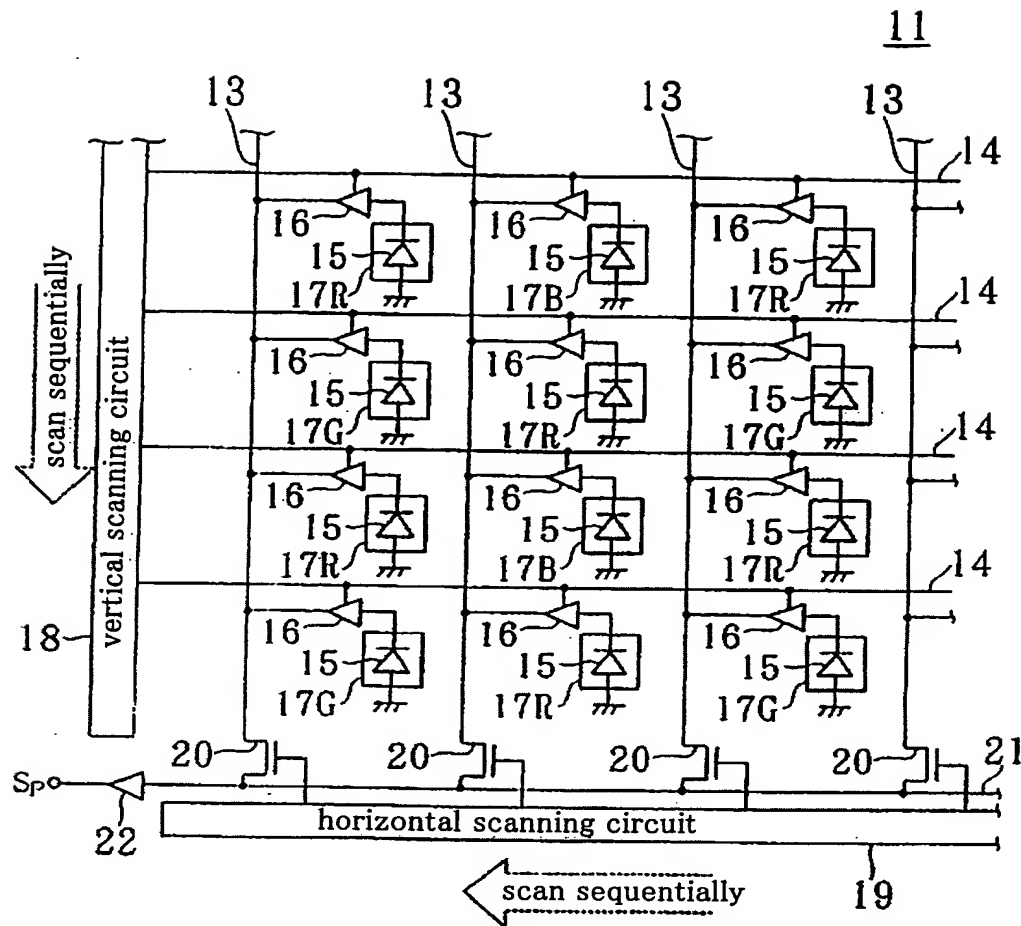


Fig.3

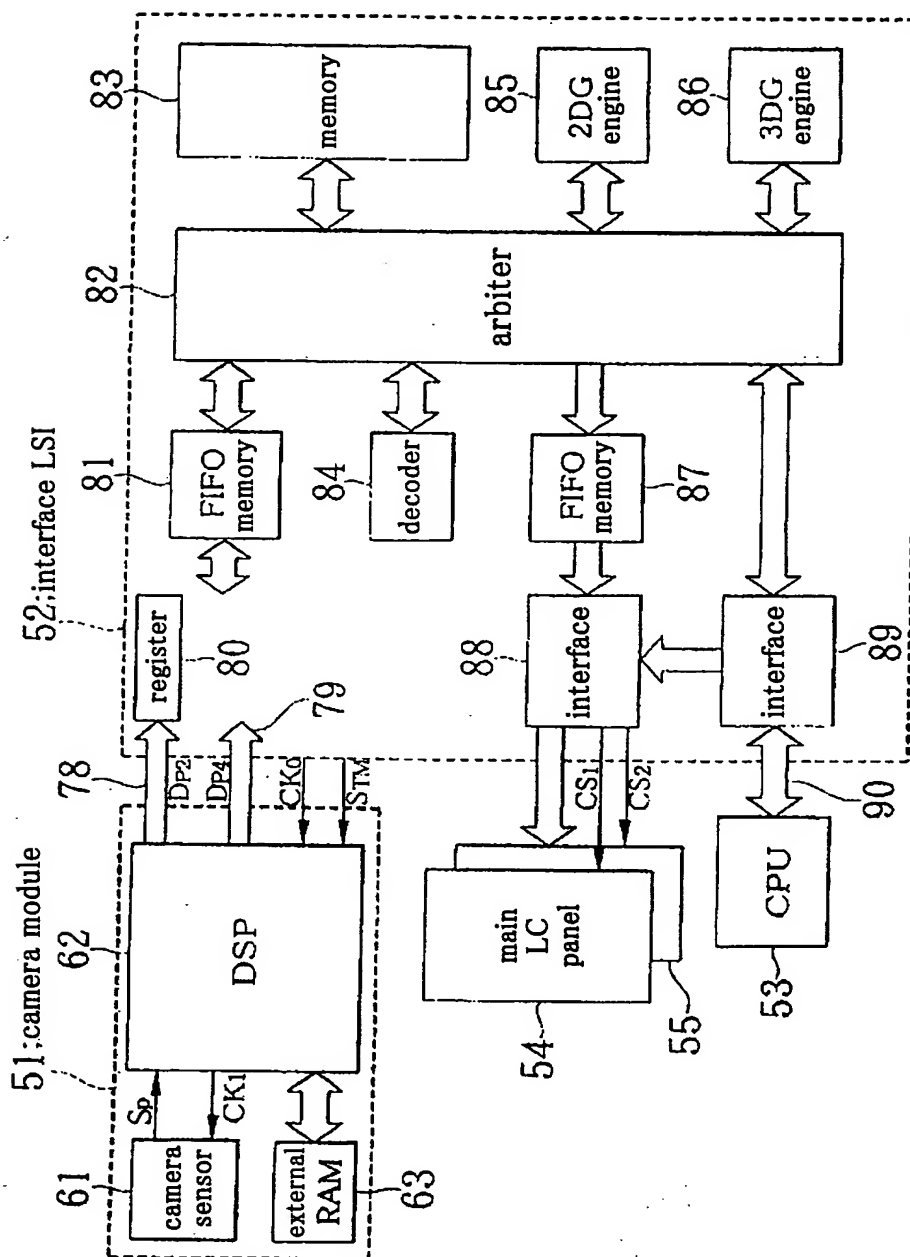


Fig.4

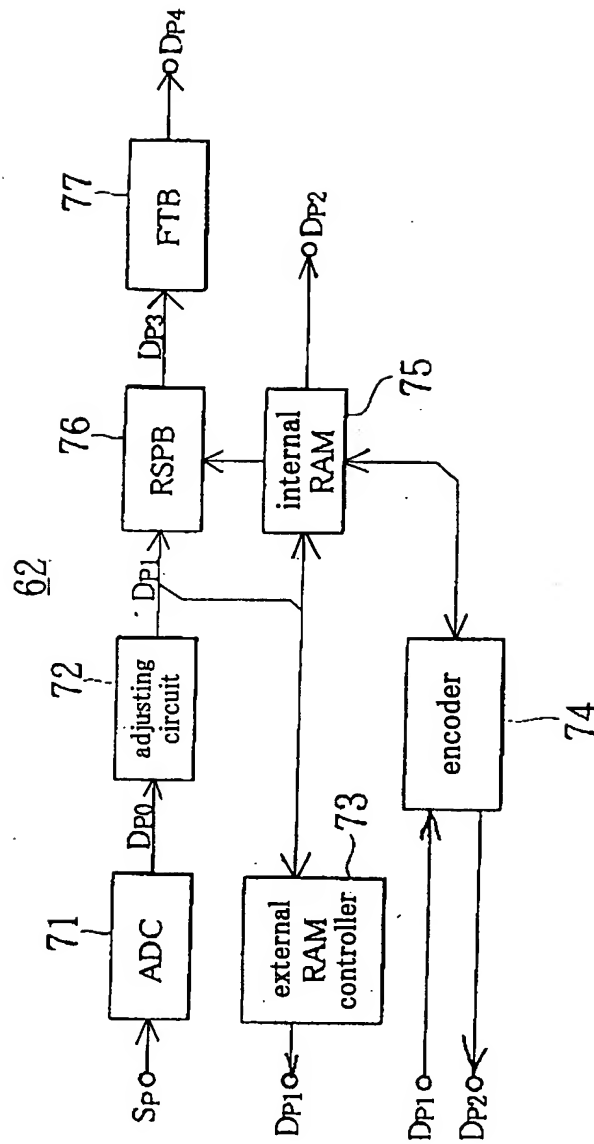


Fig. 5

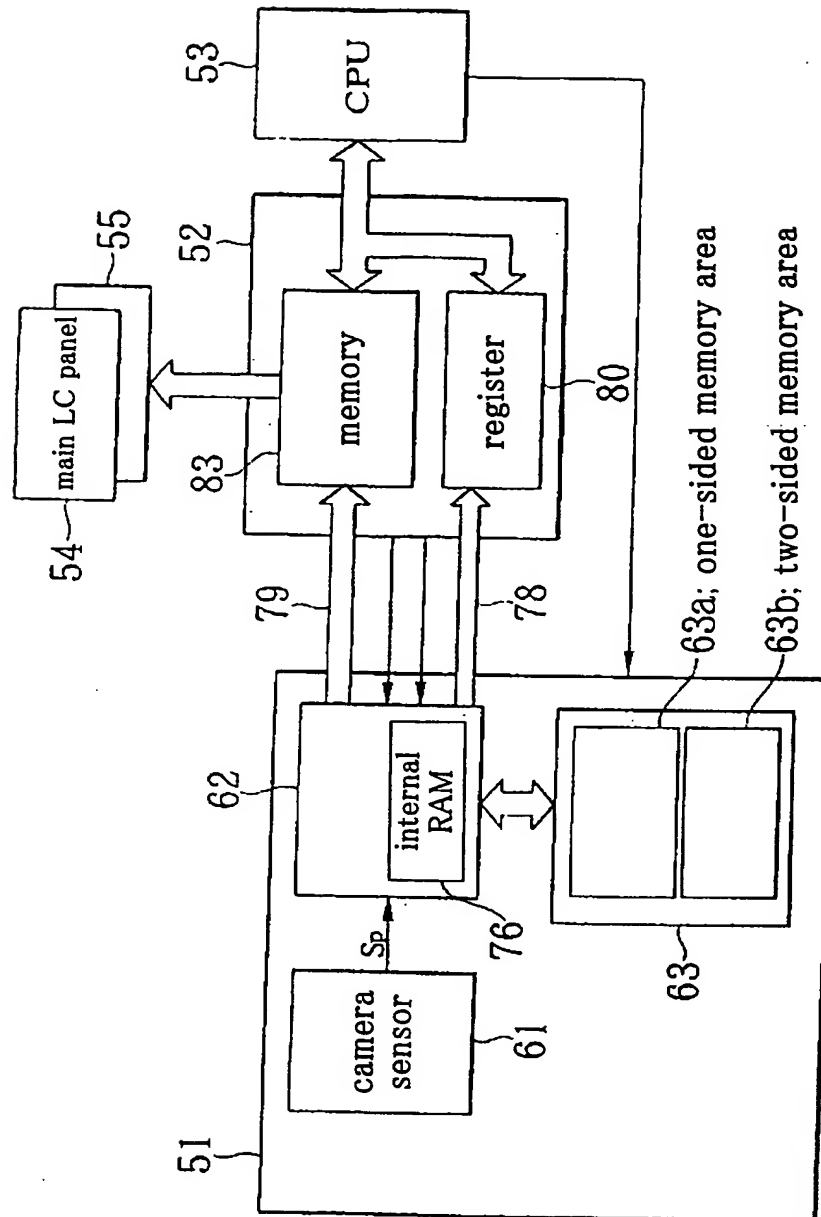


Fig.6

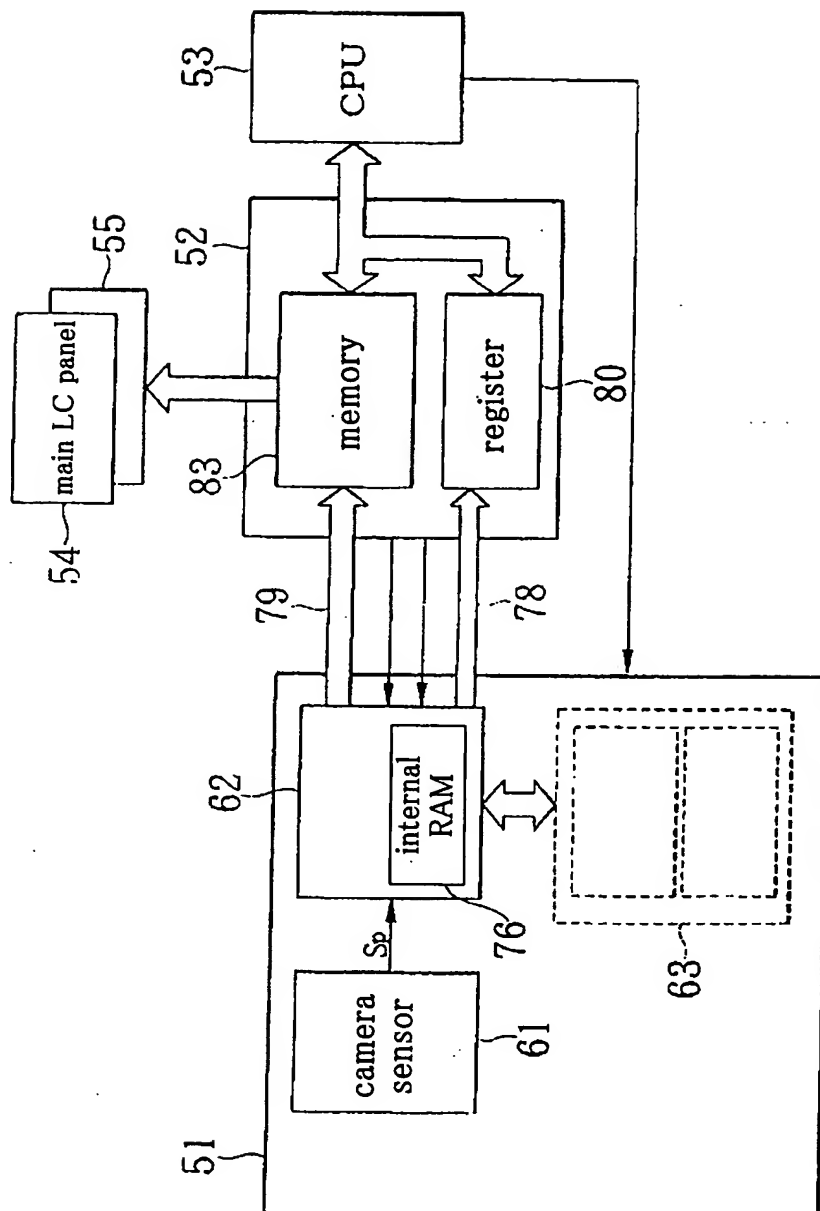


Fig.7

mode	camera module			interface LSI
	the number of bits	role of internal RAM	role of external RAM	
mobile camera mode	320 × 240	compression coding processing buffer frame buffer data buffer	inactive state	<ul style="list-style-type: none"> • operate on 48MHz clock • supply 12MHz clock to camera module
digital camera mode	1280 × 960	compression coding processing buffer	frame buffer data buffer	<ul style="list-style-type: none"> • operate on 48MHz clock • supply 48MHz clock to camera module

Fig.8

mode	the number of bits	circuit	prior art	embodiment
mobile camera mode	320 × 240	camera module	input about 48MHz clock	input about 12MHz clock
		interface LSI	operate on about 200MHz clock	operate on about 48MHz clock
digital camera mode	1280 × 960	camera module	input about 48MHz clock	input about 48MHz clock
		interface LSI	operate on about 200MHz clock	operate on about 48MHz clock

Fig.9

	the number of gates	
	prior art	embodiment
camera module	about 2,000	about 2,200
interface LSI	about 3,000	about 500
total	about 5,000	about 2,700